

FIG. 1

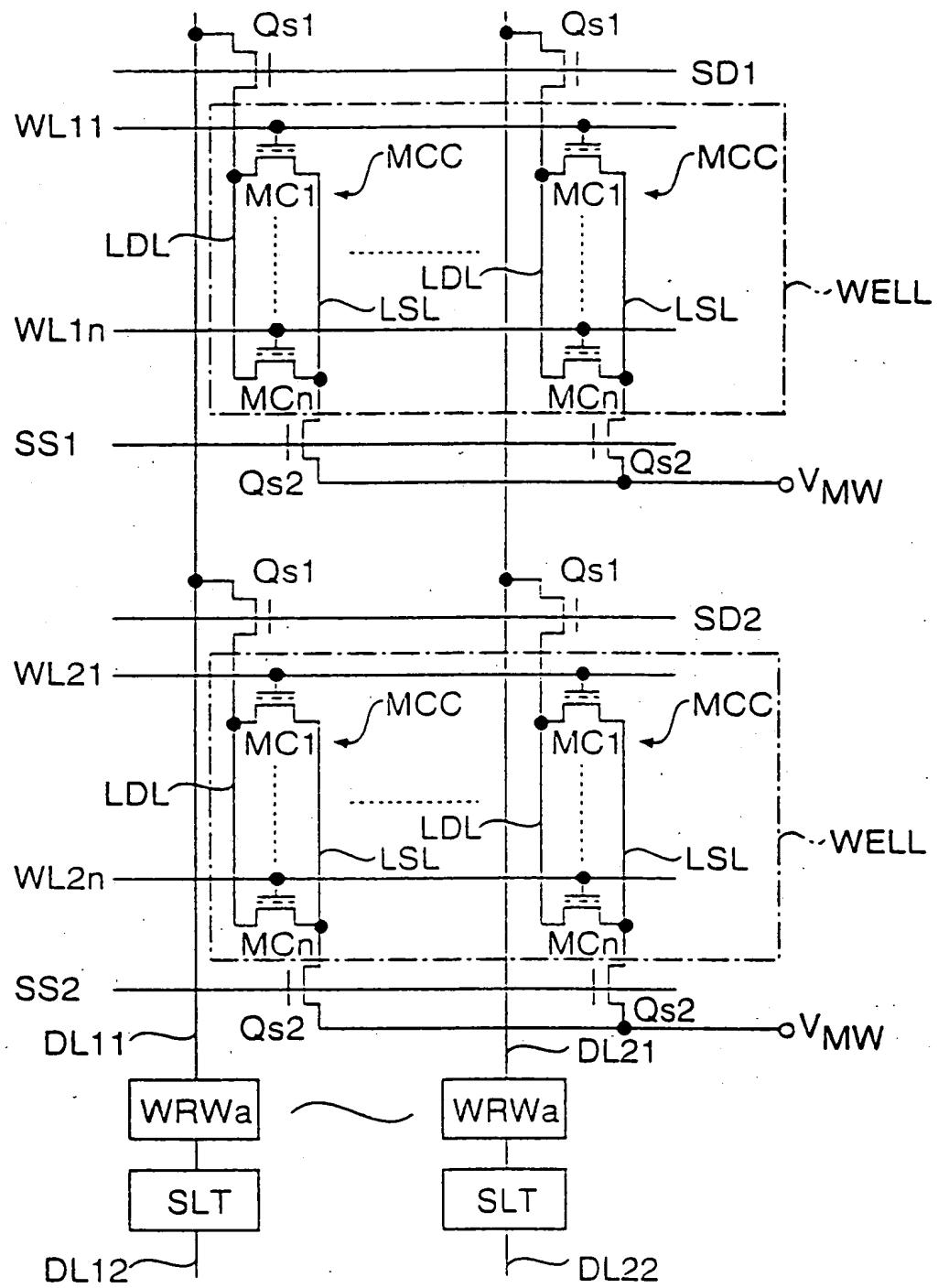


FIG. 2

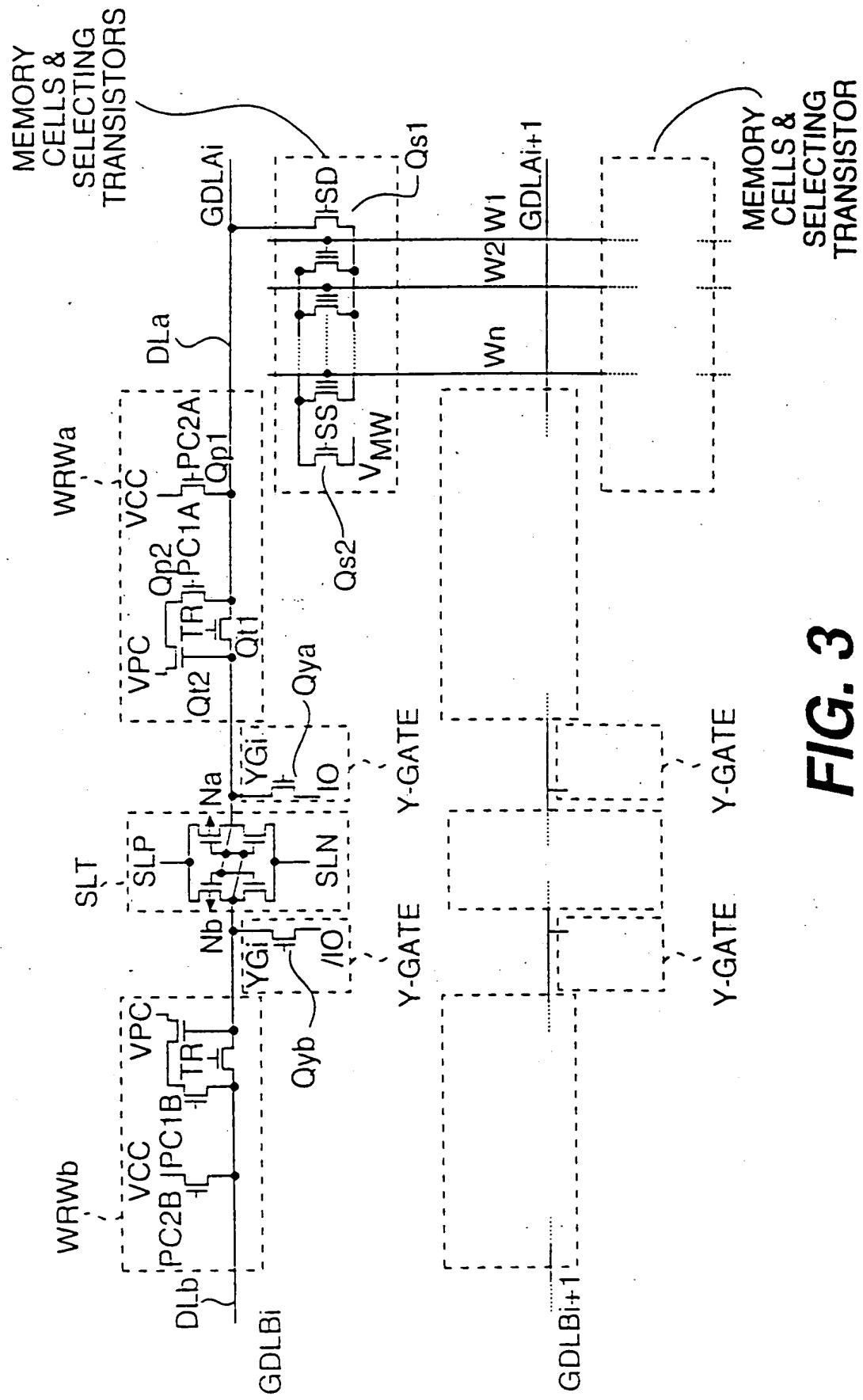


FIG. 3

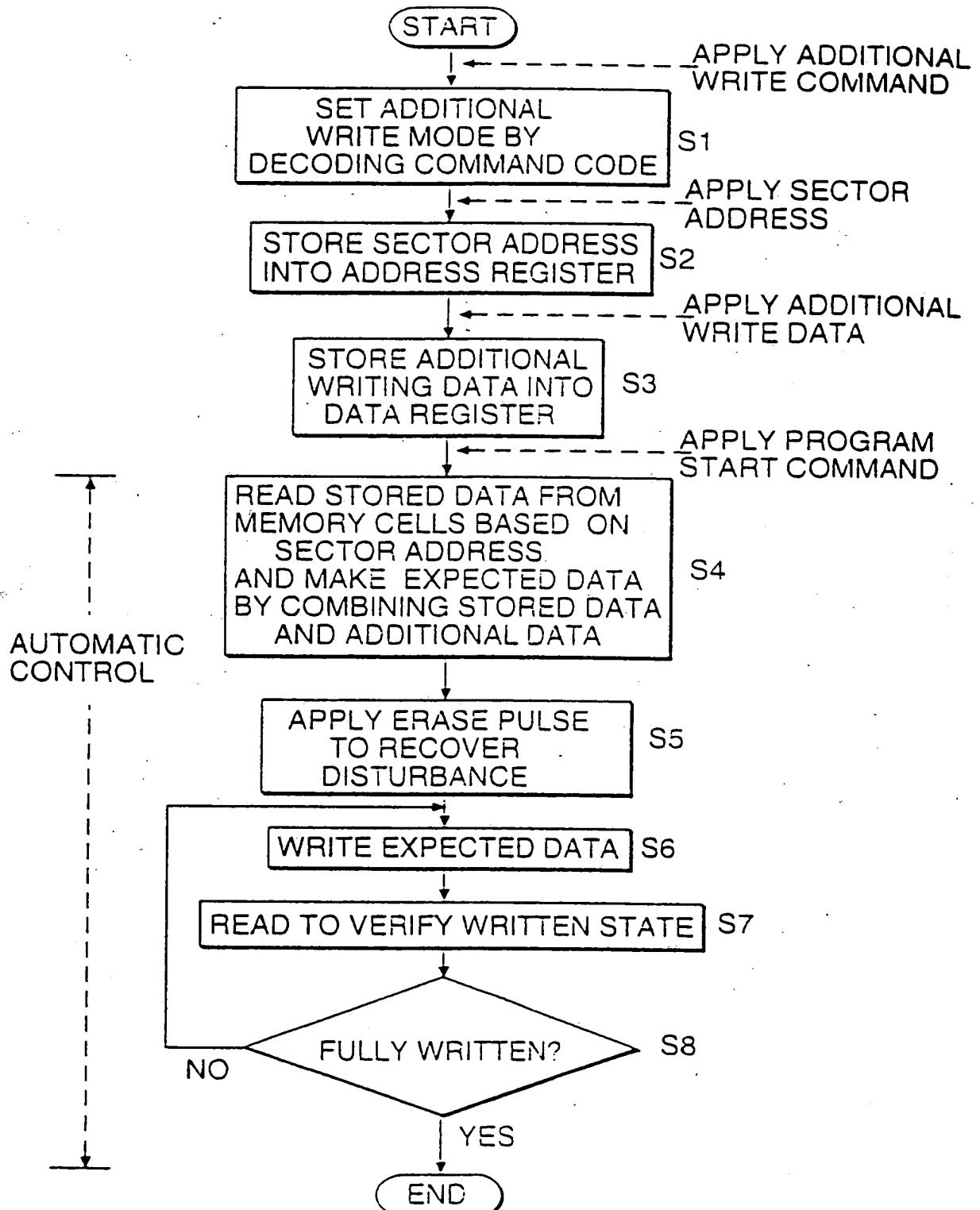


FIG. 4

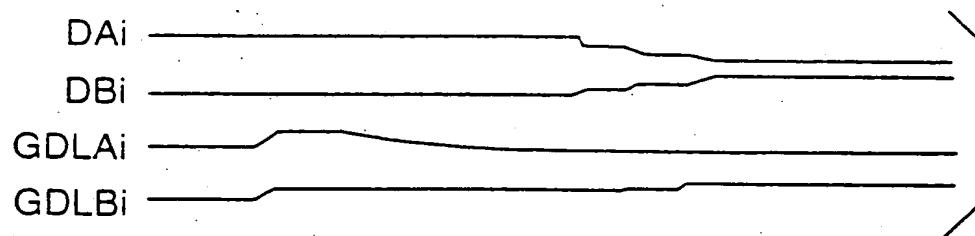
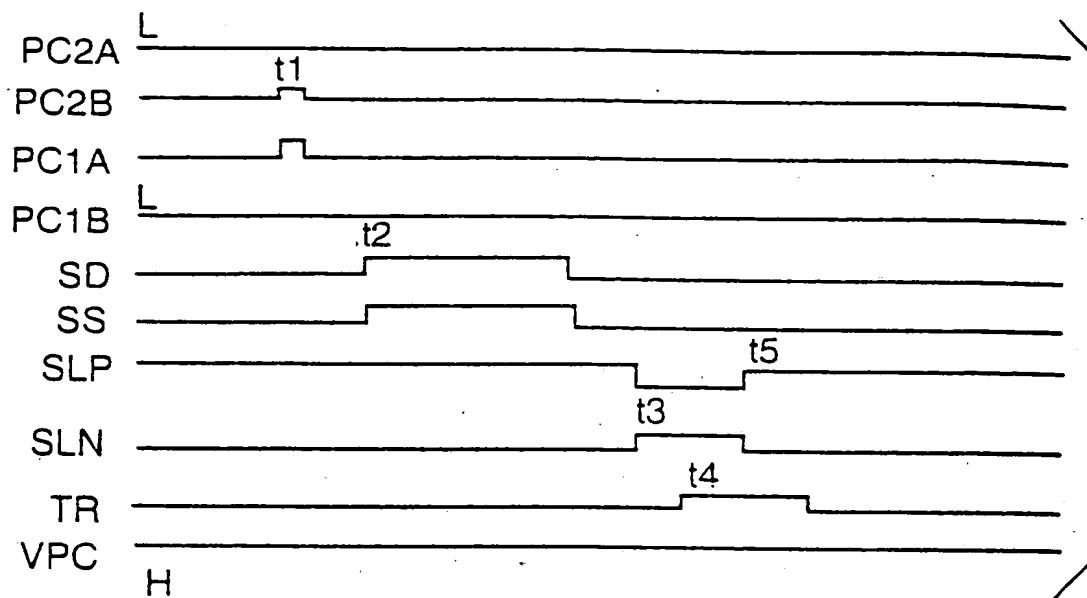


FIG. 6(a)

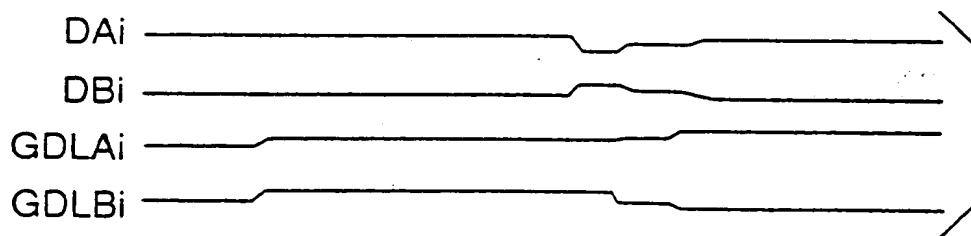


FIG. 6(b)

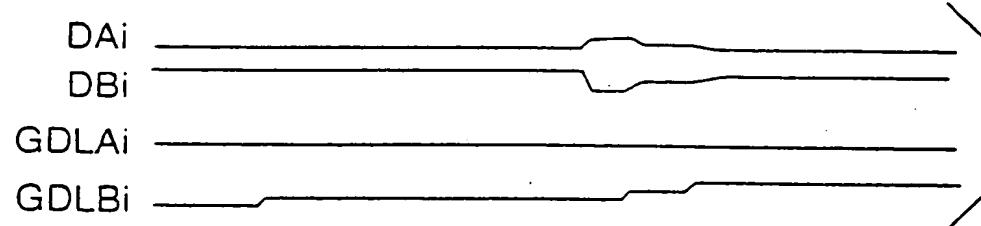


FIG. 6(c)

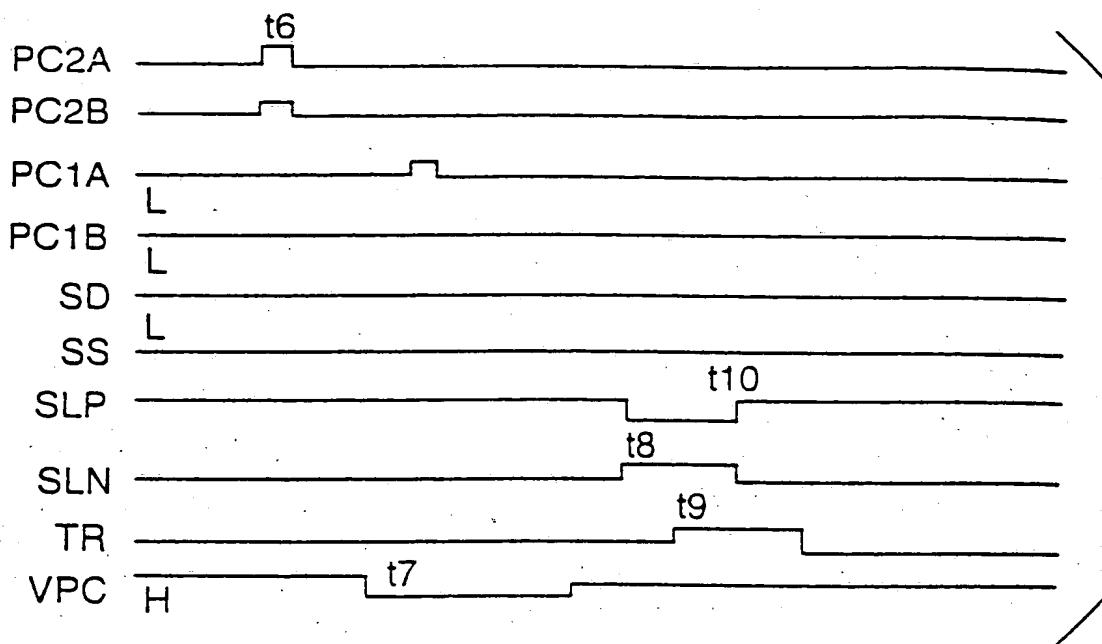


FIG. 7

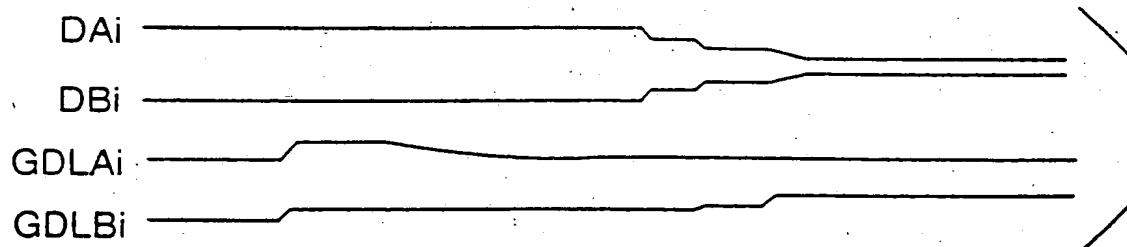


FIG. 8(a)

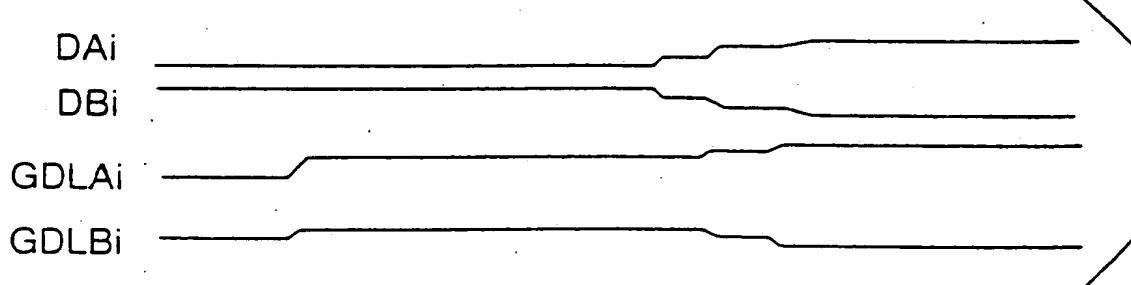


FIG. 8(b)

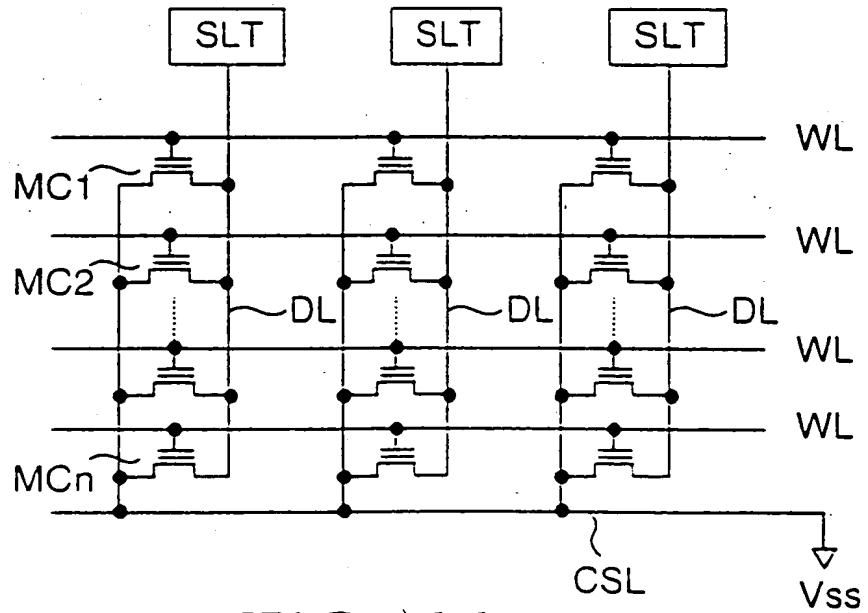
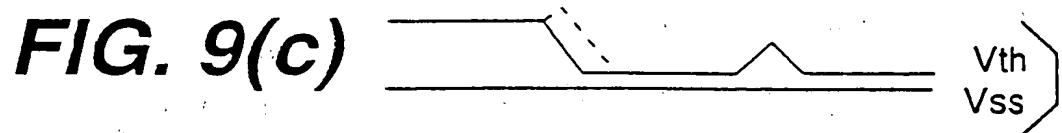
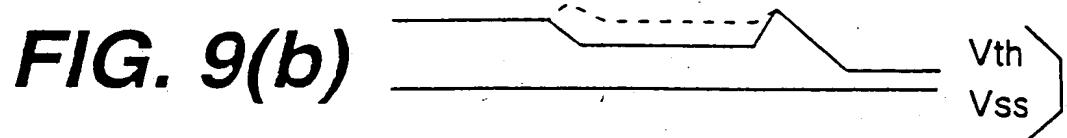
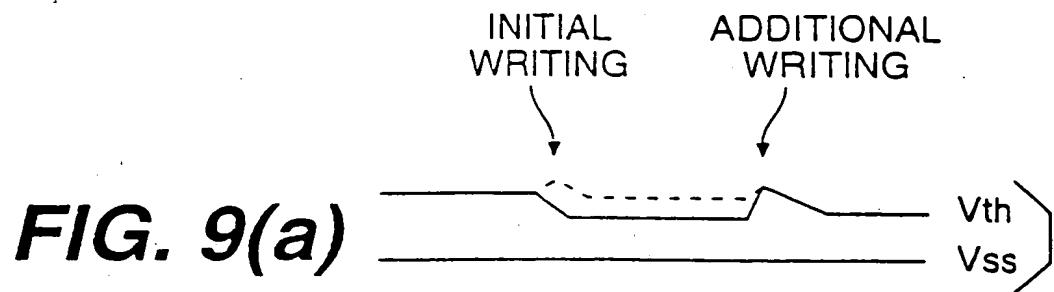
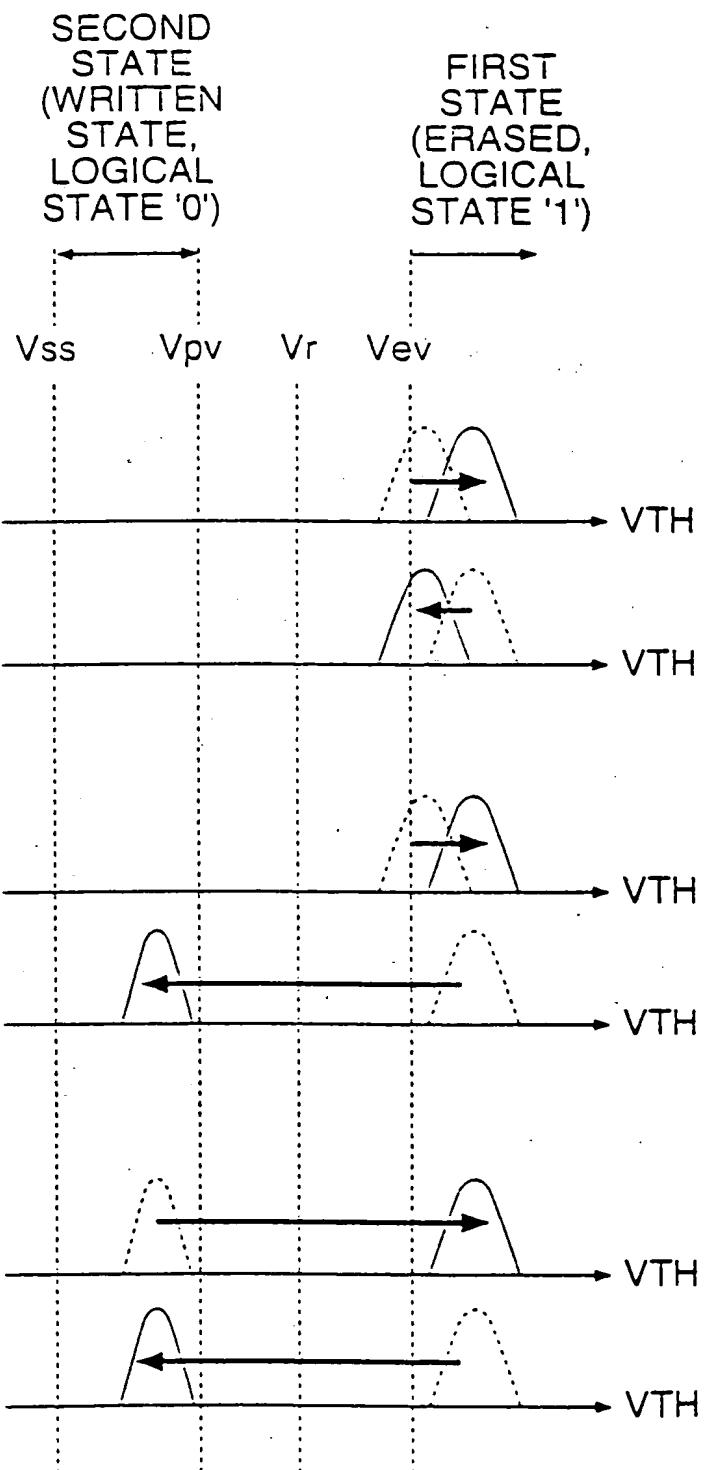
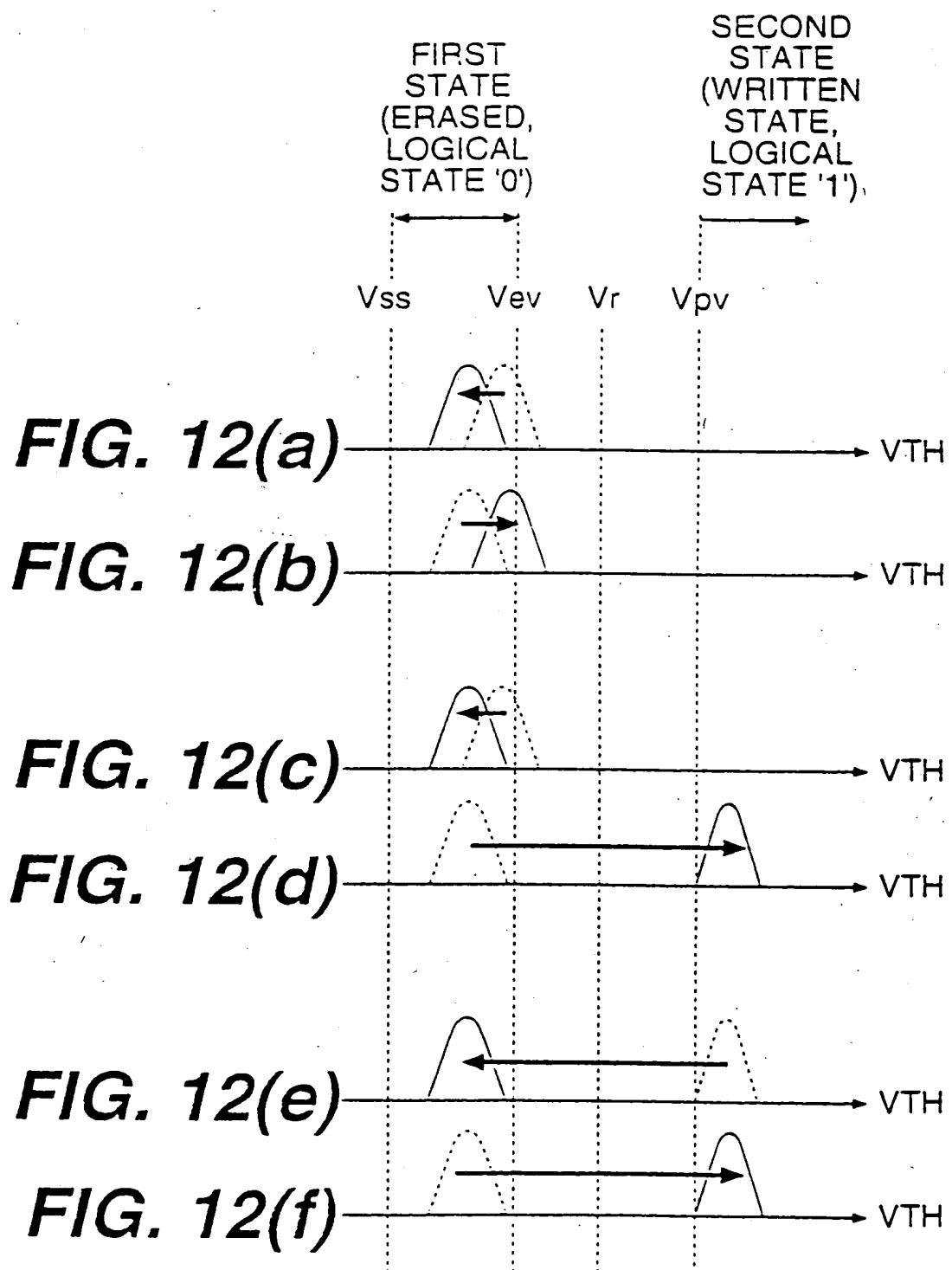
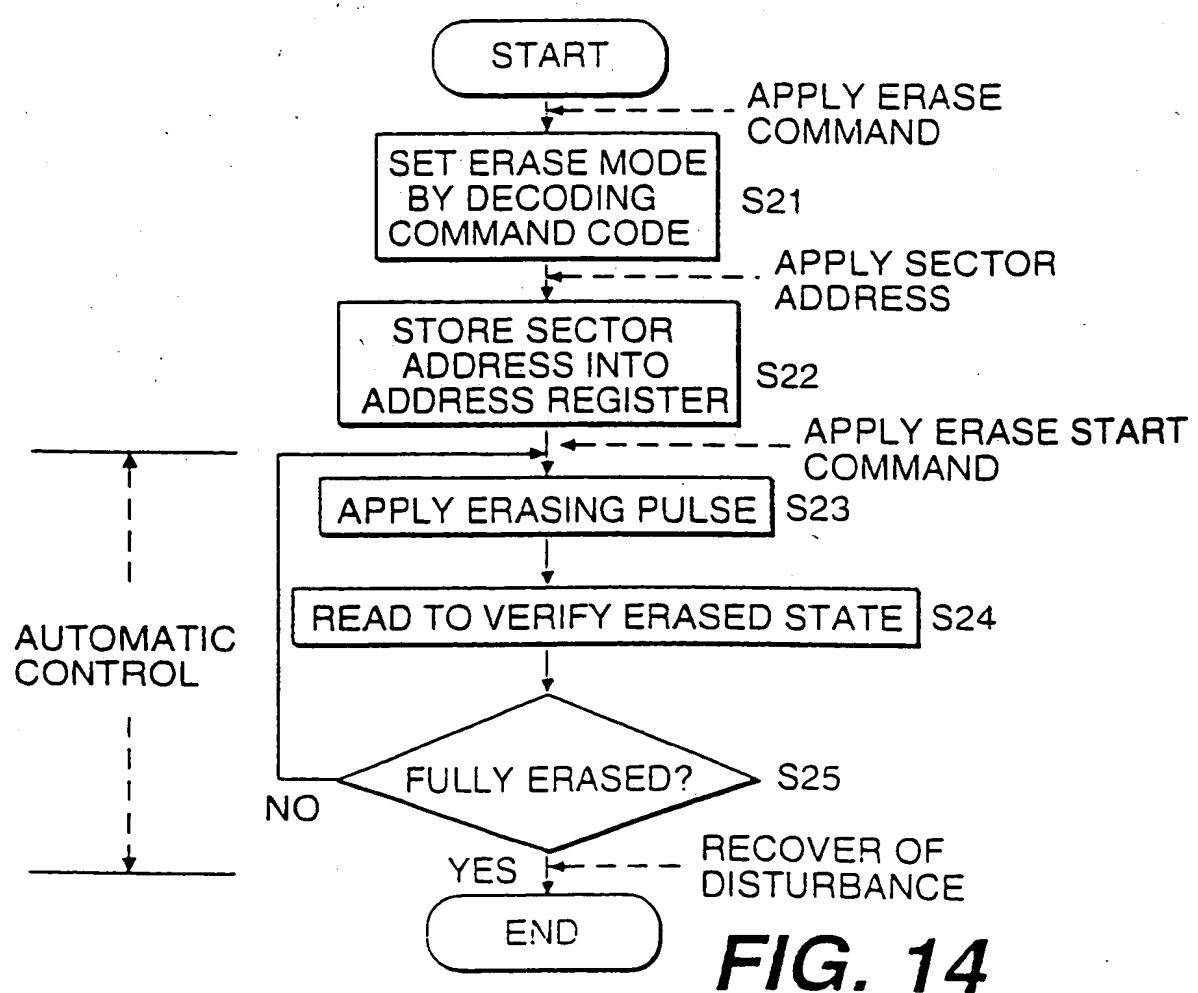
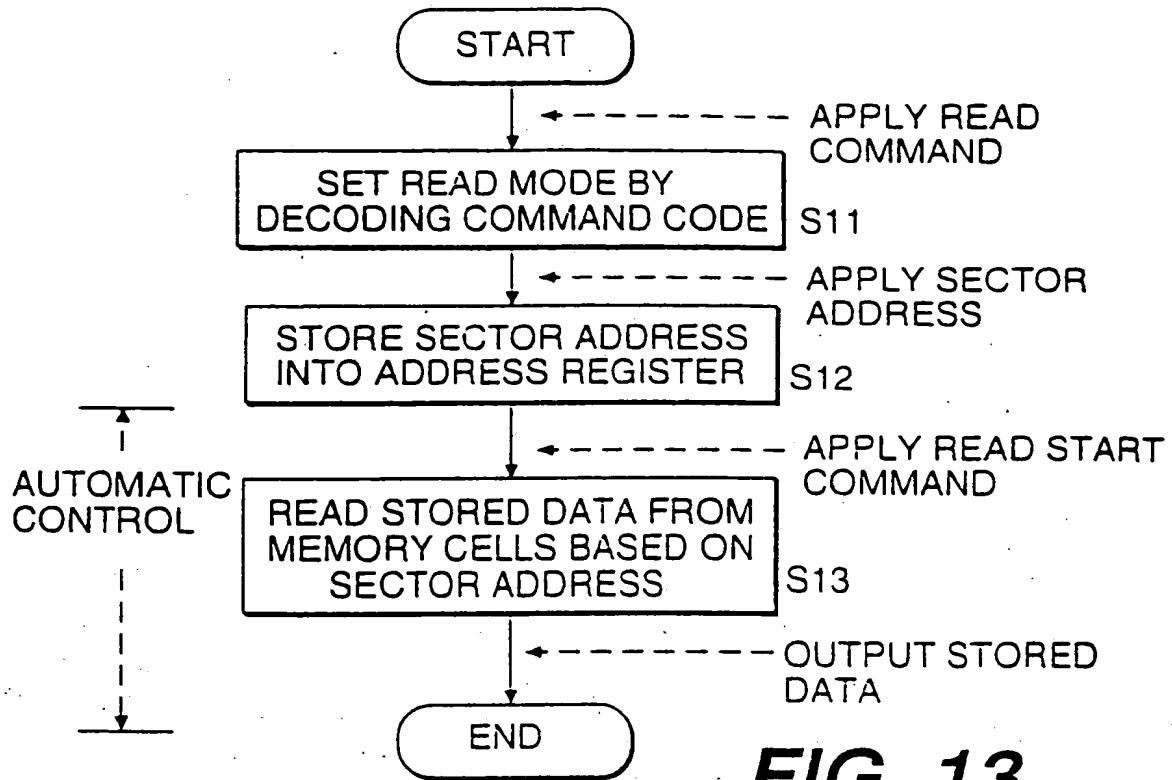


FIG. 11







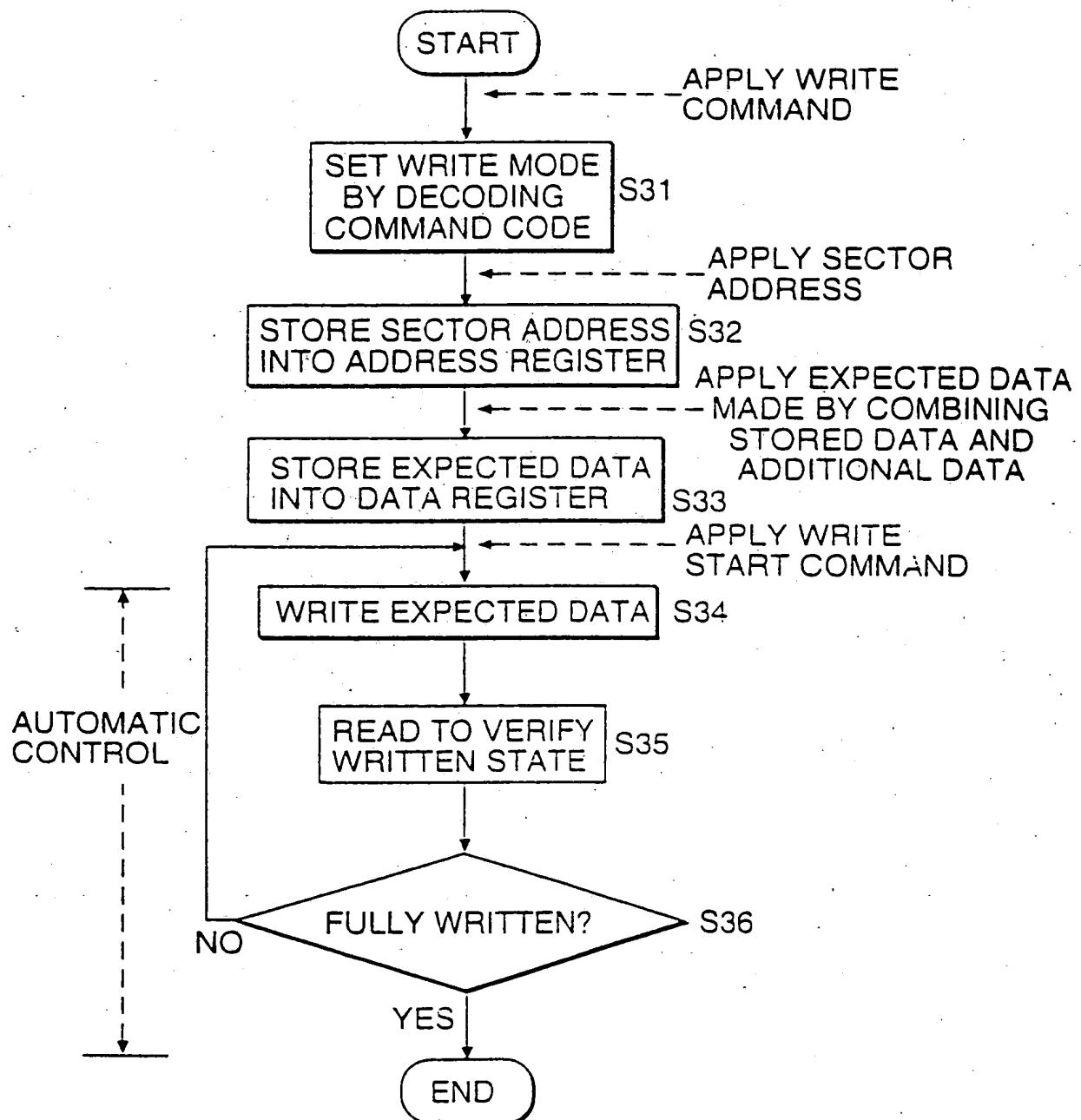


FIG. 15

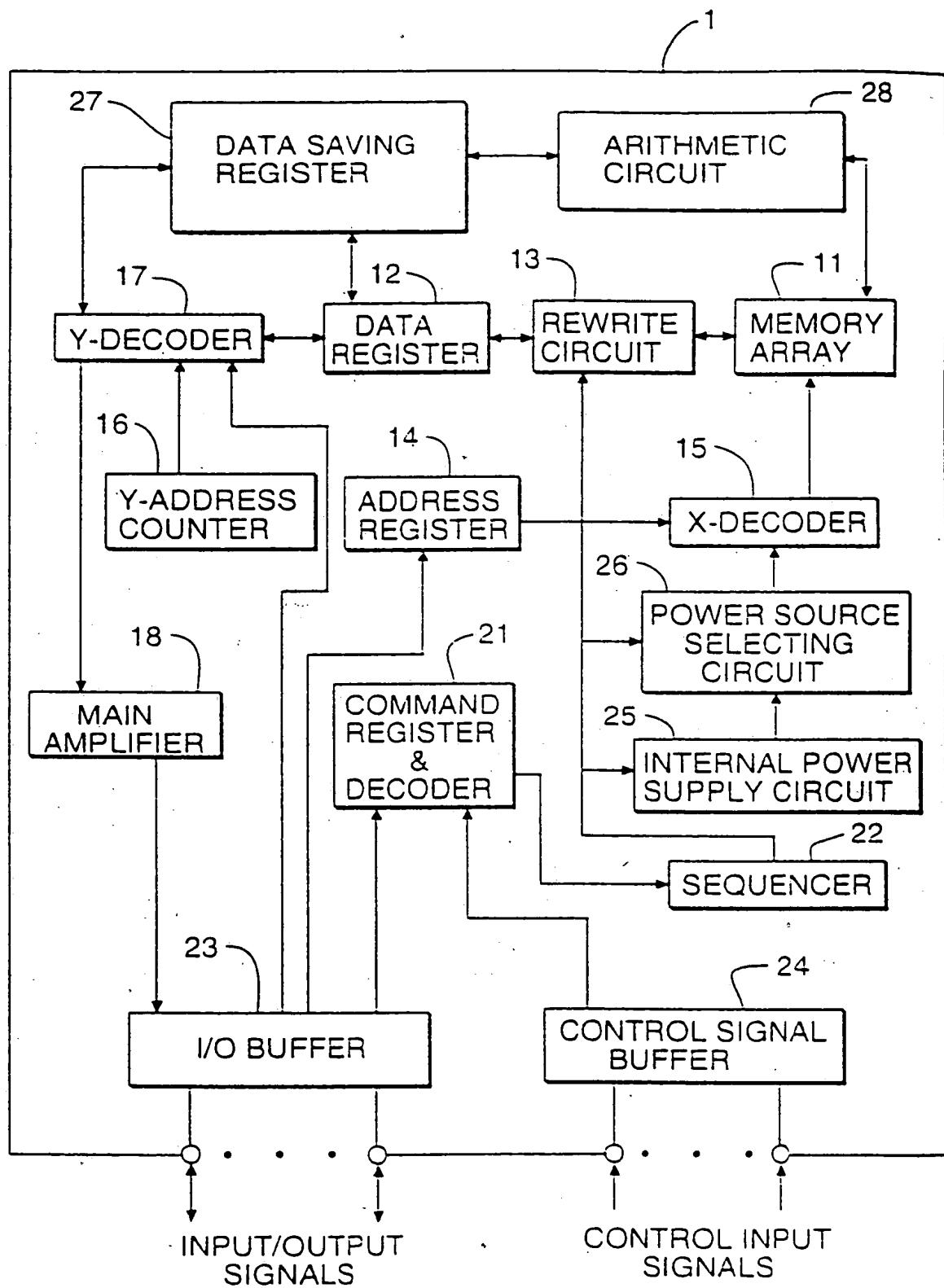


FIG. 16

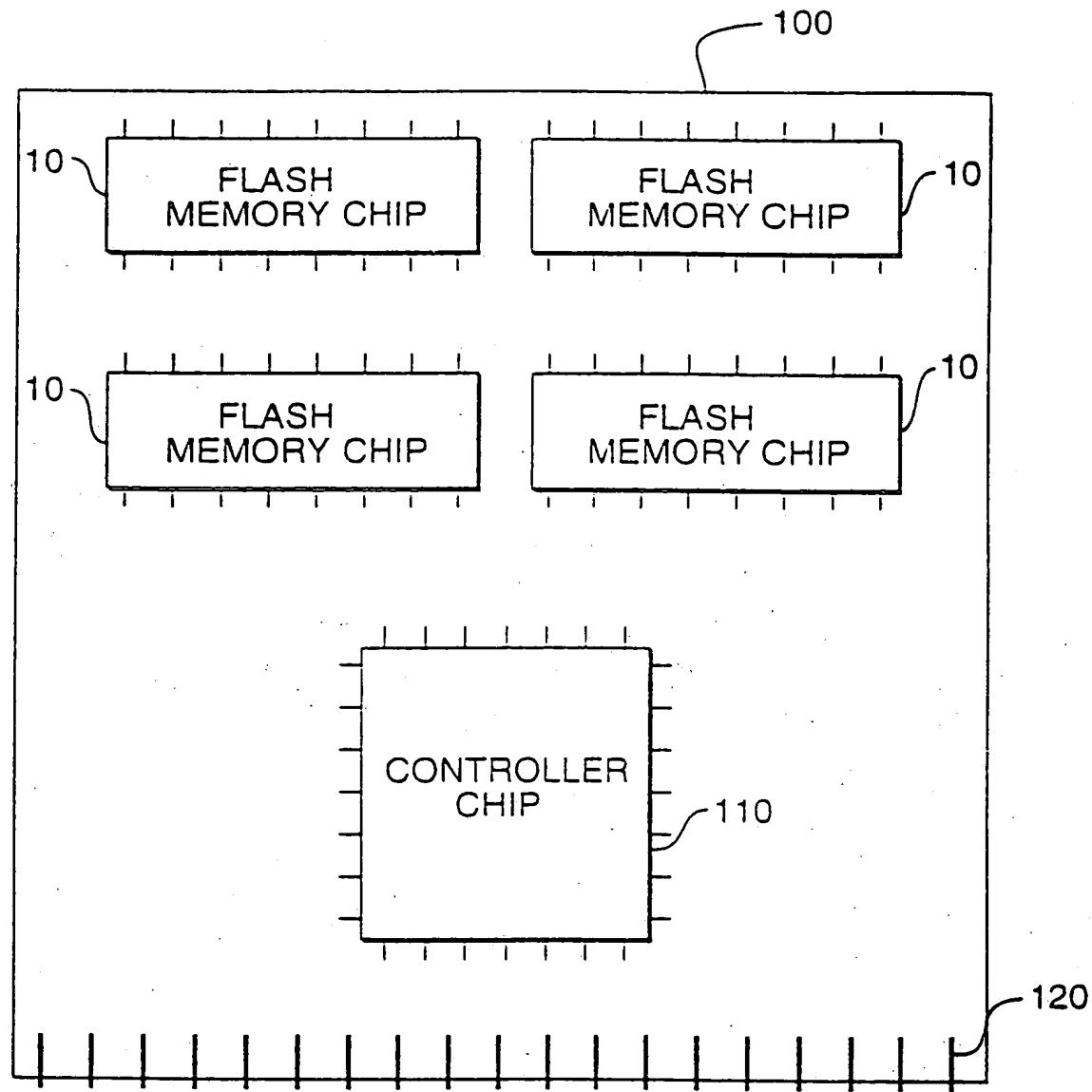


FIG. 17

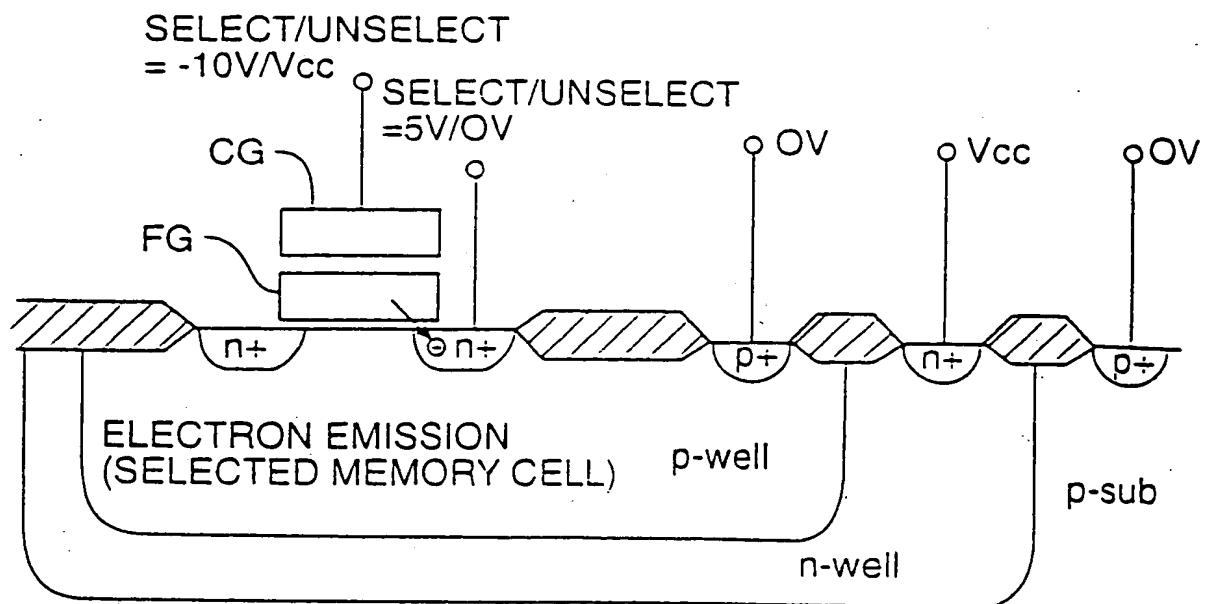


FIG. 18

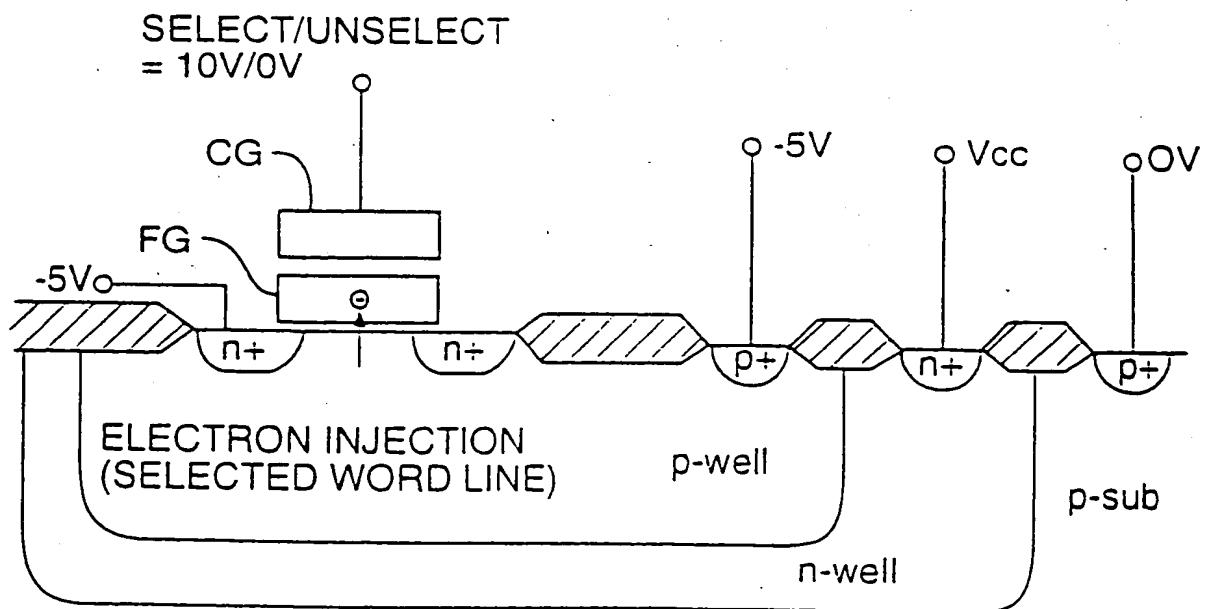


FIG. 19

PRIOR ART

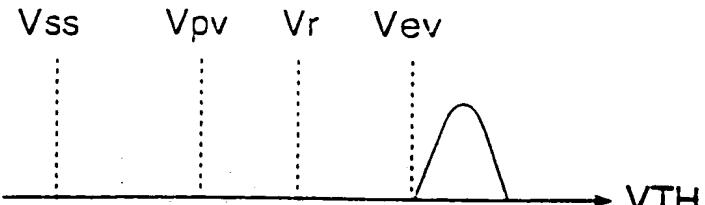


FIG. 20(a)

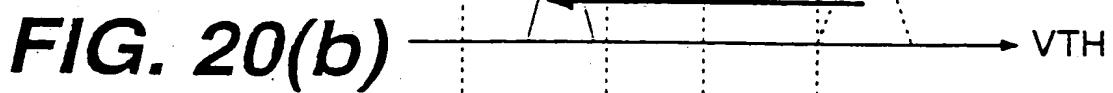


FIG. 20(b)



FIG. 20(c)



FIG. 20(d)

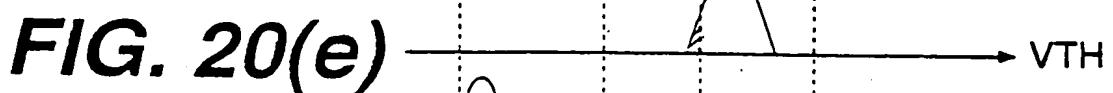


FIG. 20(e)



FIG. 20(f)

PRIOR ART

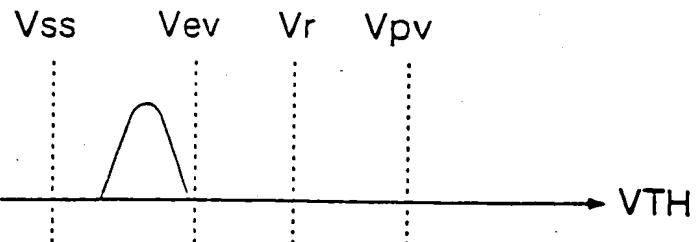


FIG. 21(a)

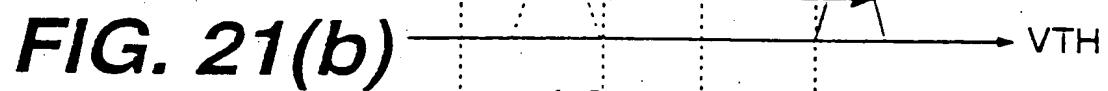


FIG. 21(b)

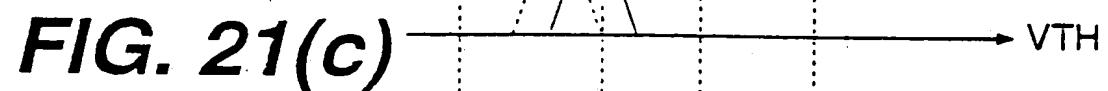


FIG. 21(c)

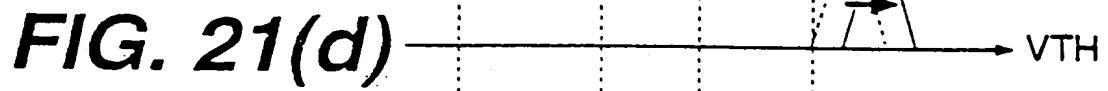


FIG. 21(d)



FIG. 21(e)

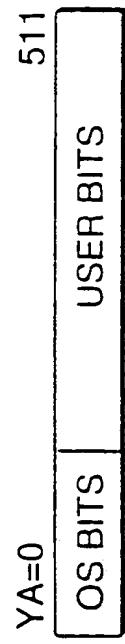


FIG. 22(a)

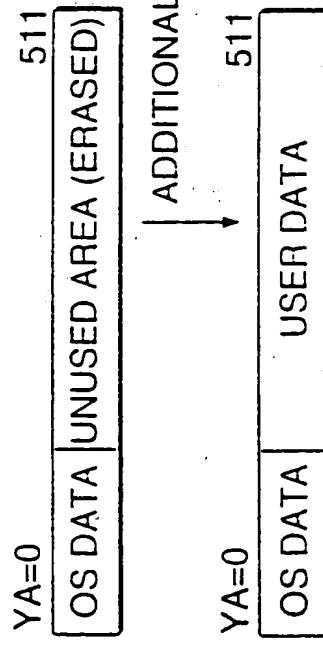


FIG. 22(b)

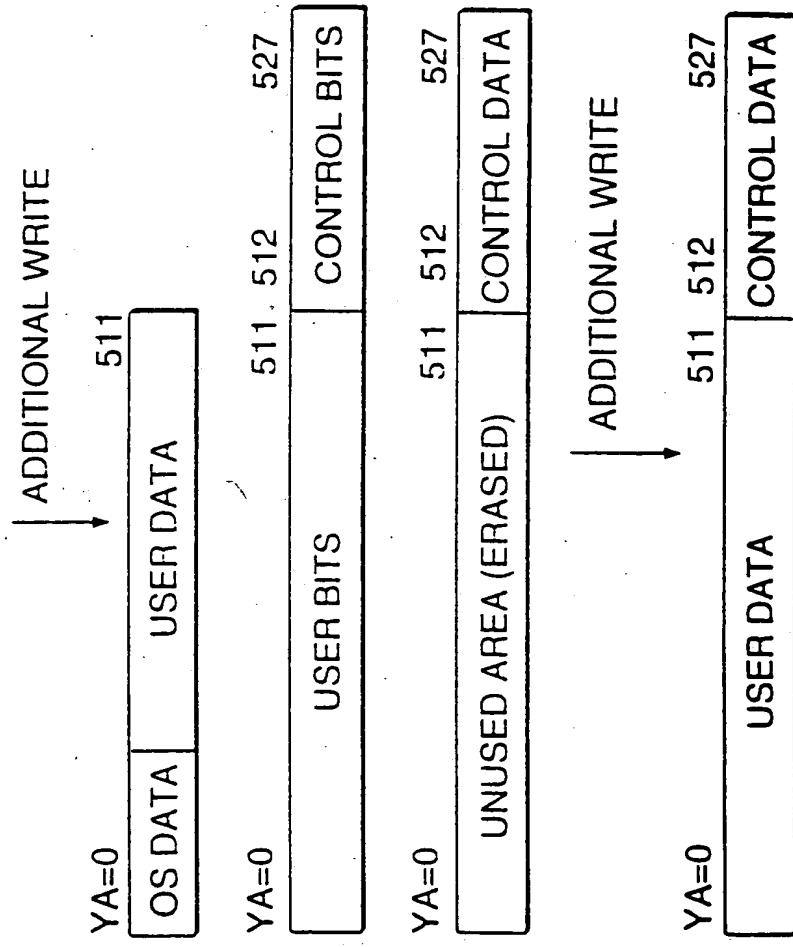


FIG. 22(d)

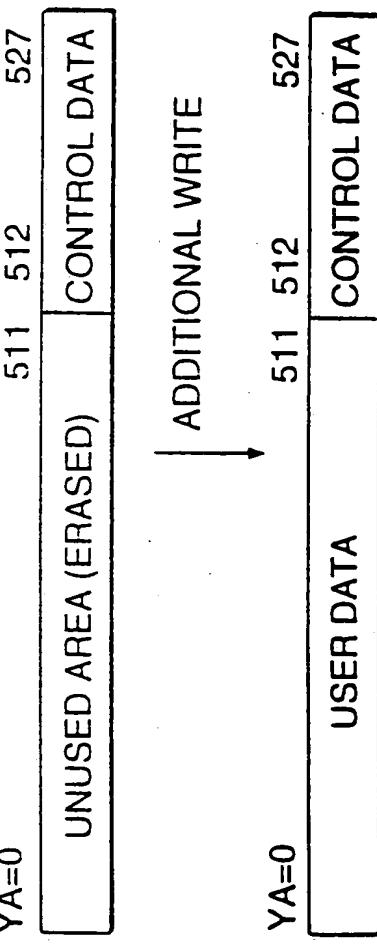


FIG. 22(e)

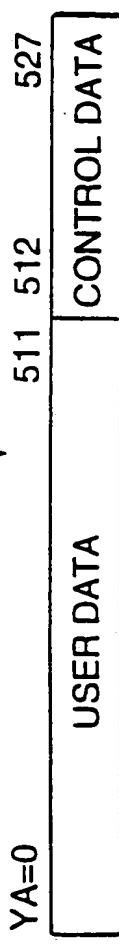


FIG. 22(f)

SECOND STATE
(WRITTEN STATE,
LOGICAL STATE '0')

FIRST STATE
(ERASED STATE,
LOGICAL STATE '1')

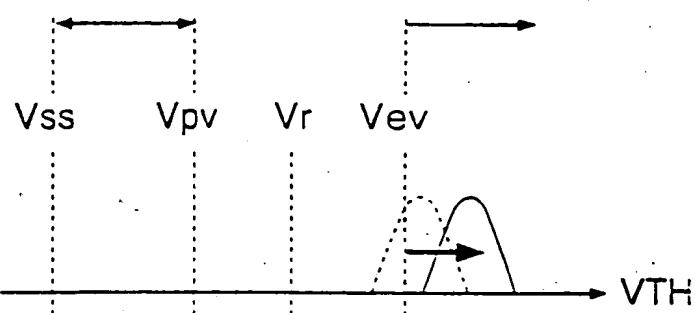
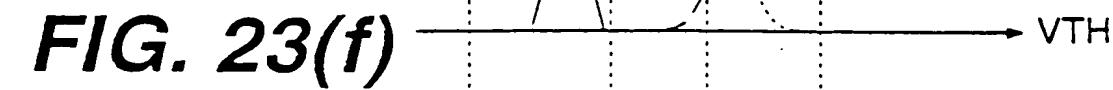
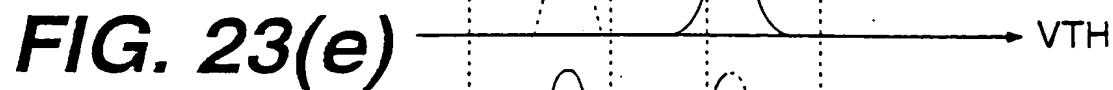
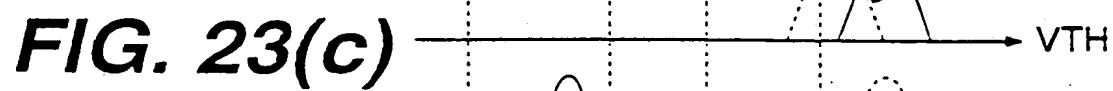


FIG. 23(a)



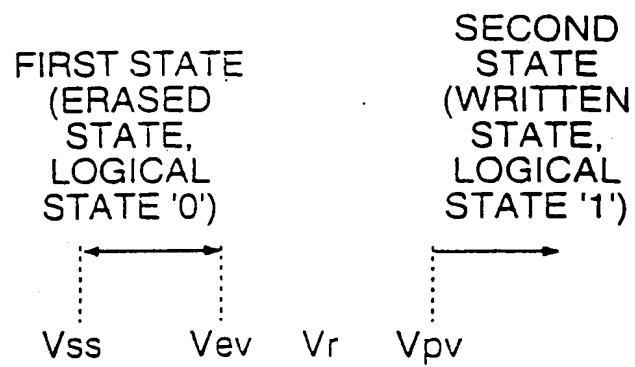


FIG. 24(a)

FIG. 24(b)

FIG. 24(c)

FIG. 24(d)

FIG. 24(e)

FIG. 24(f)